

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1162	326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:57
L2	9	326/39.ccls. and fgpa	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:17
L3	94	326/39.ccls. and (pld adj architecture)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:08
L4	88	326/39.ccls. and (pld adj architecture and multiplexer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:58
L5	44	326/39.ccls. and (pld adj architecture and multiplexer and SRAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:59
L6	0	326/39.ccls. and (pld adj architecture adj multiplexer adj SRAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:59
L7	1	326/39.ccls. and (fgpa adj architecture)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:09
L8	152	(fgpa adj architecture) and 326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:18
L9	0	(fgpa adj architecture adj multiplexer) and 326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:18
L10	0	(fgpa adj architecture adj mux) and 326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:19

L11	124	(fpga adj architecture and multiplexer) and 326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:19
L12	52	(fpga adj architecture and multiplexer and SRAM) and 326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:24
L13	0	fpga and 716/16/ccls	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:24
L14	0	fpga and 716/16/ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:24
L15	0	fpga and 716/16/ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:24
L16	288	fpga and 716/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 10:24

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	18	fpga adj multiplexer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:19
L2	2	"08948306"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:34
L3	0	fpga adj architecture adj multiplexer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:36
L4	450	716/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:39
L5	21	fpga and 716/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:40
L6	5	fpga and multiplexer and 716/16. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:42
L7	540	fpga adj architecture	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:43
L8	73	(fpga adj architecture) and 716/16. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:48
L9	0	(fpga adj architecture) and 716/16. ccls and pd@<"20010418"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:44
L10	0	(fpga adj architecture) and 716/16. ccls	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/23 09:44